

CLAIMS:

1. A filter circuit for suppressing high frequency noise in an FM-receiver, the circuit comprising a first filter branch and a second filter branch connected in parallel, the first filter branch comprising a high pass filter and a multiplier for multiplying a signal passing through the first filter branch and a control signal indicative of high frequency noise,
5 and the second filter branch comprising a low pass filter.
2. The filter circuit according to claim 1, wherein the multiplier is arranged downstream from the high pass filter.
- 10 3. The filter circuit according to claim 1, wherein the high pass filter is devoid of a capacitor connected to ground.
4. The filter circuit according to claim 1, wherein the low pass filter comprises a series resistance and a capacitor connected to ground, and wherein the high pass filter
15 comprises a series capacitor and a resistor connected to ground.
5. The filter circuit according to claim 1, further comprising an adder for adding a signal from the first filter branch and a signal from the second filter branch.
- 20 6. The filter circuit according to claim 1, wherein both the high pass filter and the low pass filter have a cut-off frequency ranging between 0.1 and 2.0 kHz, preferably between 0.2 and 1.1 kHz.
7. An apparatus, comprising a noise detector circuit coupled to a filter circuit
25 according to claim 1.
8. The apparatus according to claim 7, which is an FM receiver further comprising a stereo decoder, wherein the filter circuit is located immediately after the stereo decoder.

9. The apparatus according to claim 7, which is an FM receiver further comprising a stereo decoder, wherein the filter circuit is located immediately before the stereo decoder.

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10. A method of suppressing high frequency noise in an FM-receiver, the method comprising:

high pass filtering a first signal component,

multiplying the first signal component by a control signal indicative of the

10 high frequency noise,

low pass filtering a second signal component, and

adding the filtered first signal component and the filtered second signal

component.